

Test generation means (client)

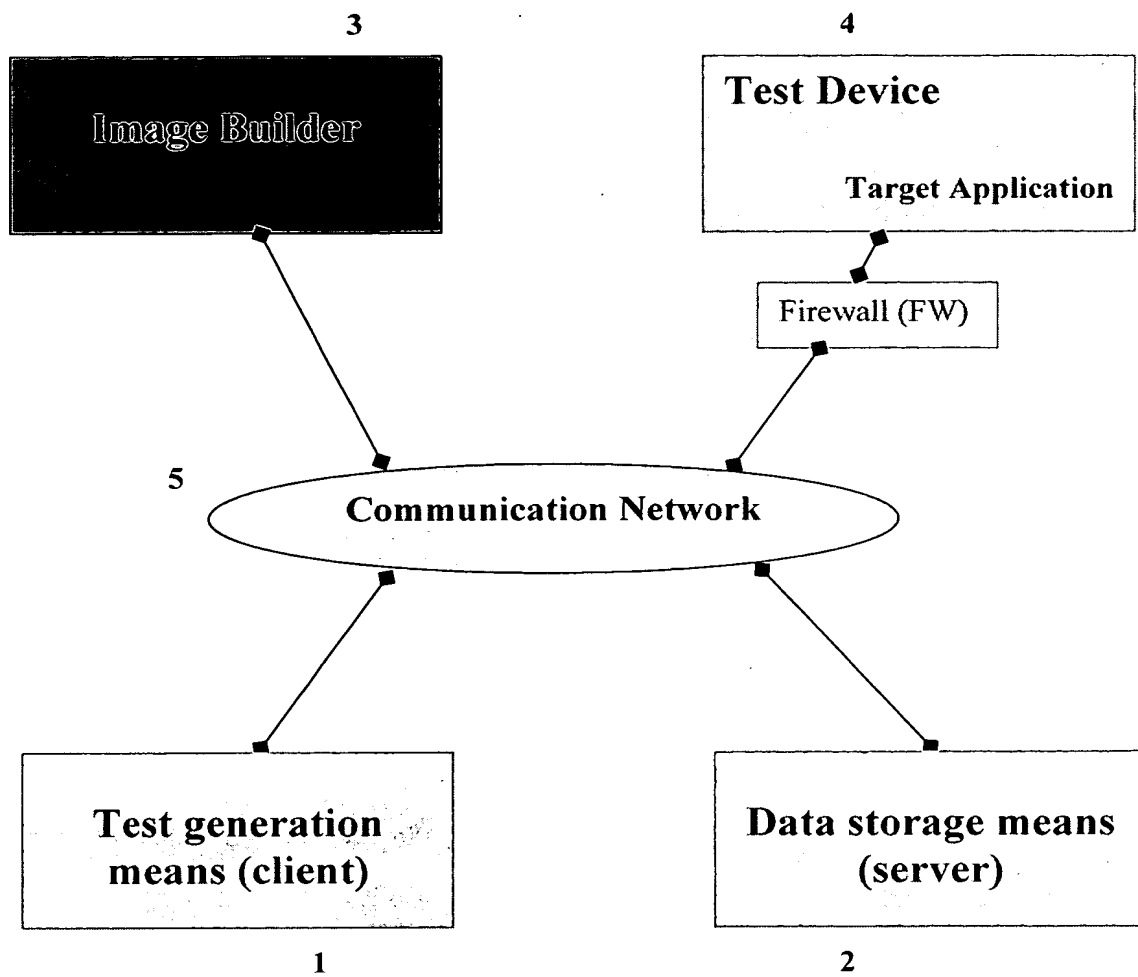


Figure 1

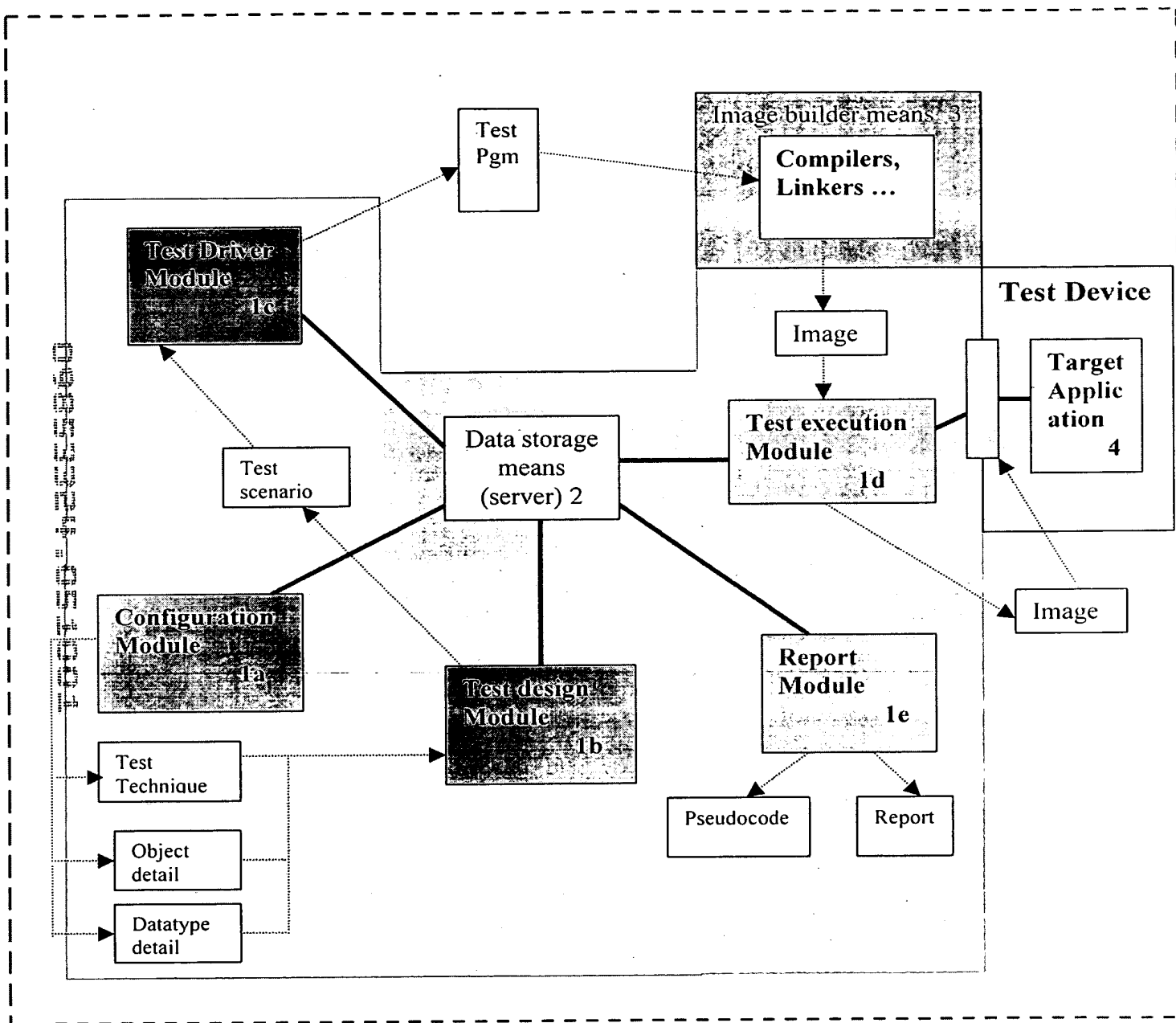


Figure 2

Test Bed

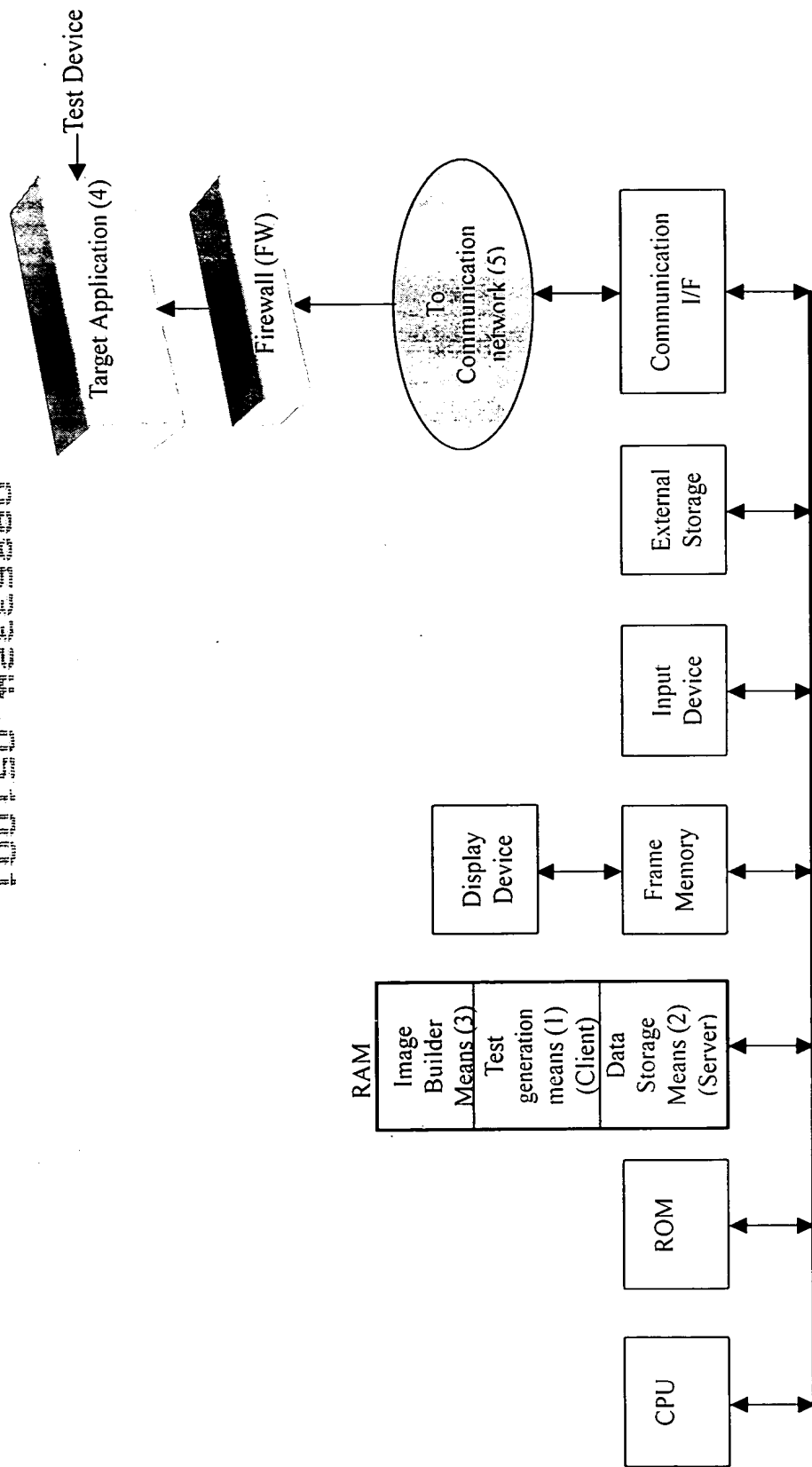


Figure 3

Figure 4 is a block diagram of a test system architecture. The system is divided into three main sections: a Test generation means (1) (Client) on the left, a Target Application (4) in the center, and a Test Device on the right. The Test generation means (1) (Client) is connected to a Communication Network (5) via a Communication I/F. The Target Application (4) is connected to the Communication Network (5) via a Firewall (FW). The Test Device is connected to the Communication Network (5) via a Communication I/F. The Communication Network (5) is a central hub that facilitates communication between the Client, the Target Application, and the Test Device. The Test generation means (1) (Client) includes a CPU, ROM, RAM, and various modules: Configuration module (1a), Test design module (1b), Test driver module (1c), Test-execution module (1d), and Report module (1e). It also includes a Display Device, Frame Memory, Input Device, External Storage, and Communication I/F. The Test Device includes a CPU, ROM, RAM, Image Builder Means (3), Frame Memory, Display Device, Input Device, External Storage, and Communication I/F. The Target Application (4) is represented by a large blacked-out area. The Communication Network (5) is a central oval connecting all components.

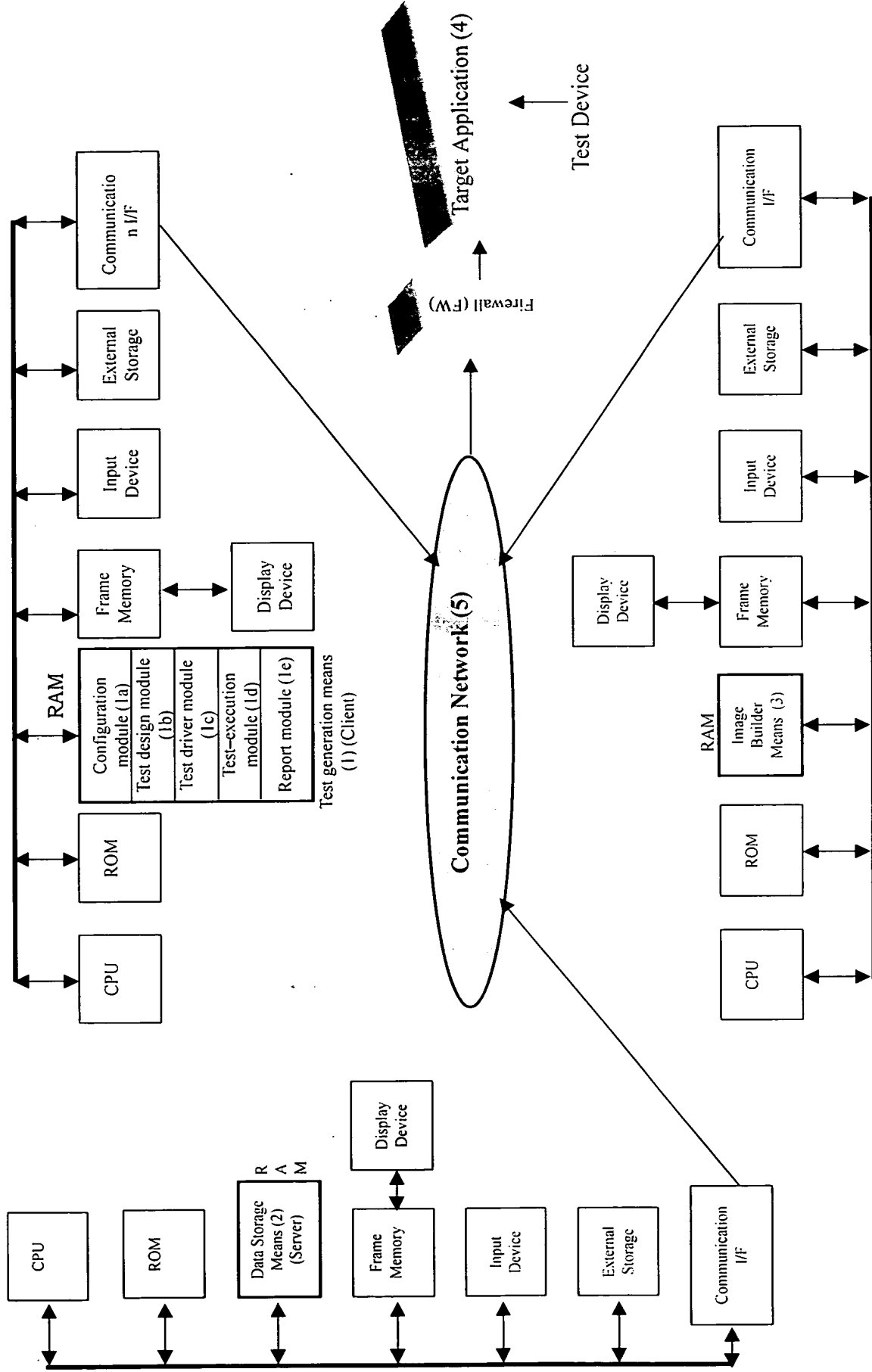


Figure 4

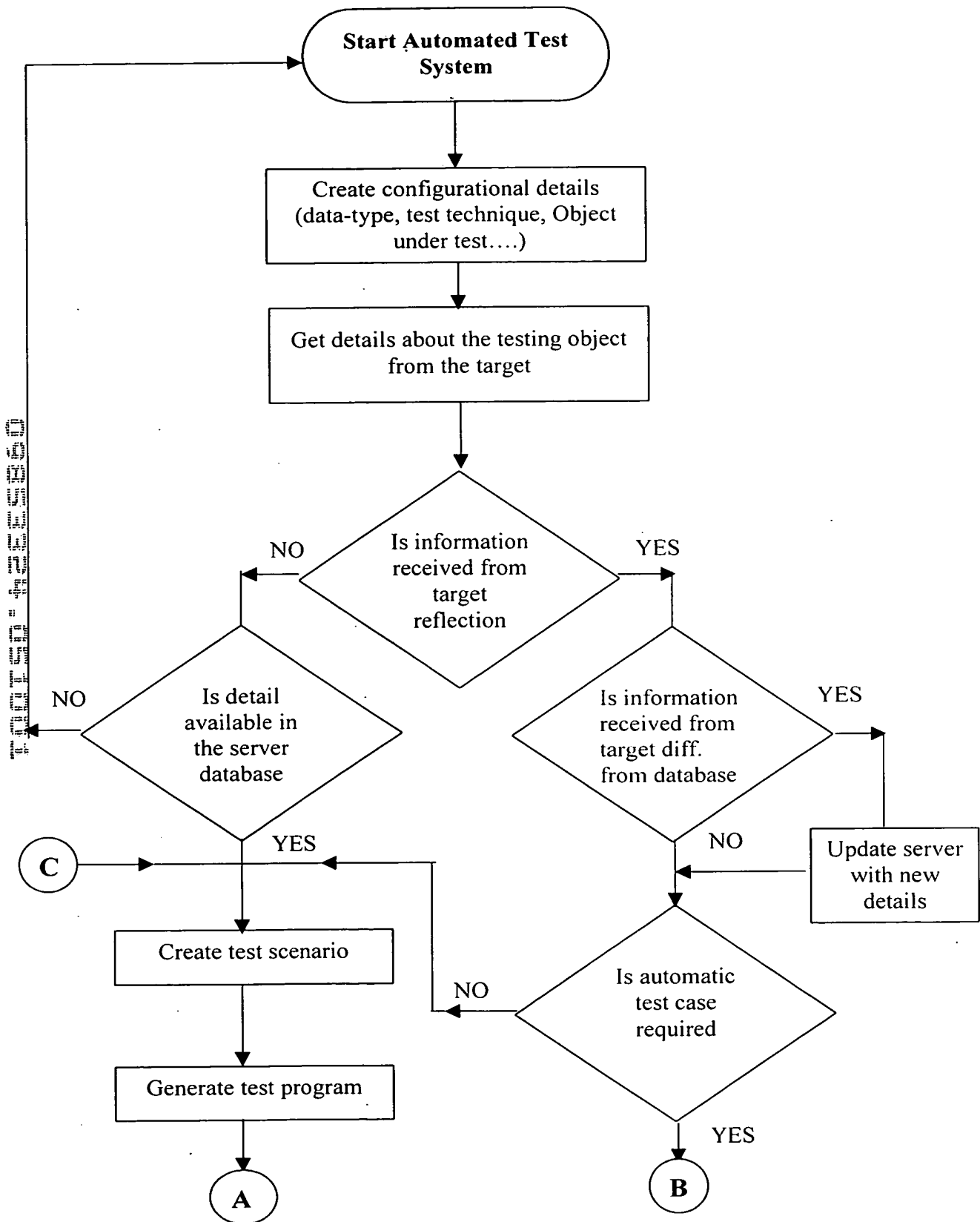


Figure 5a

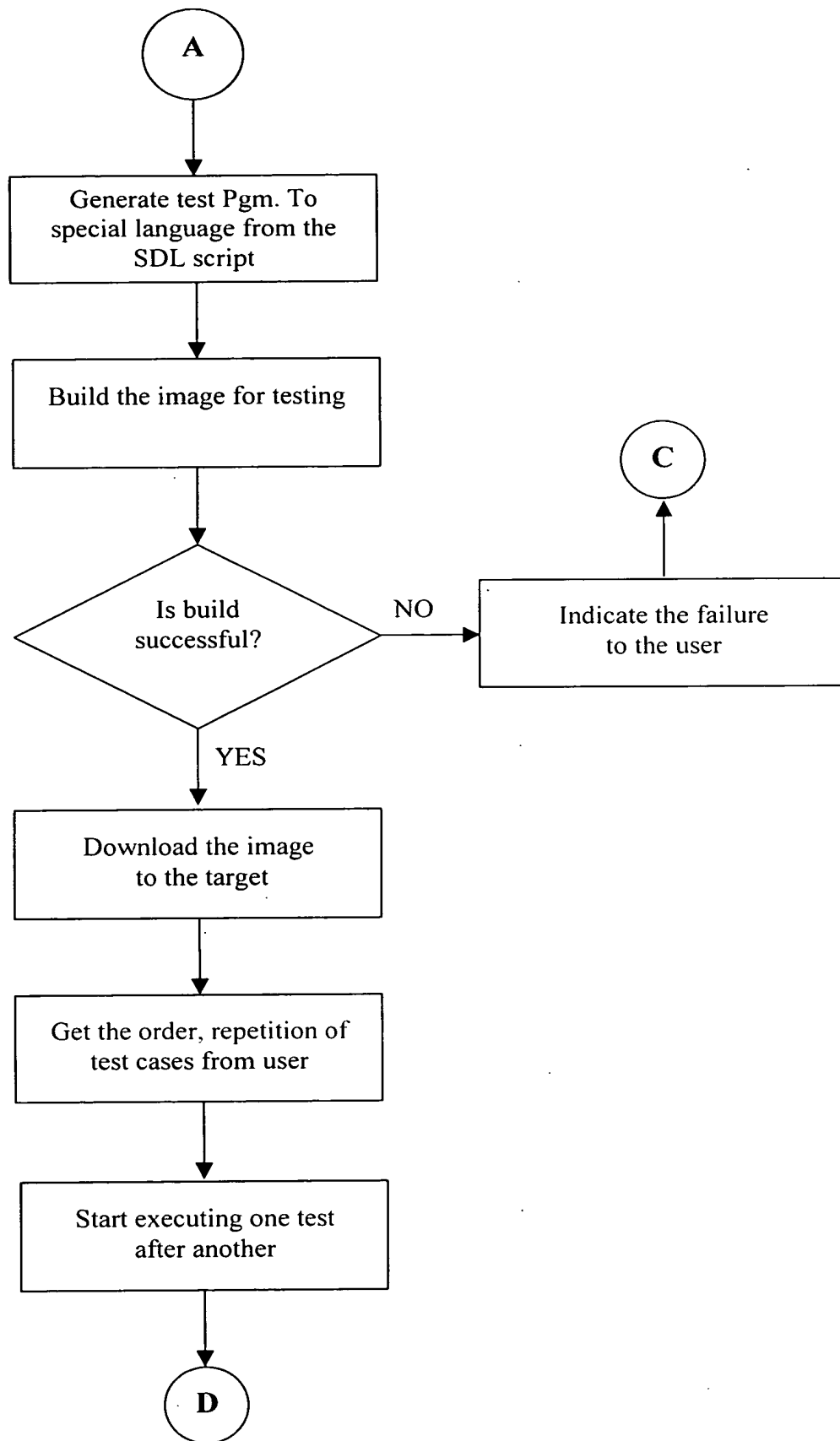


Figure 5b

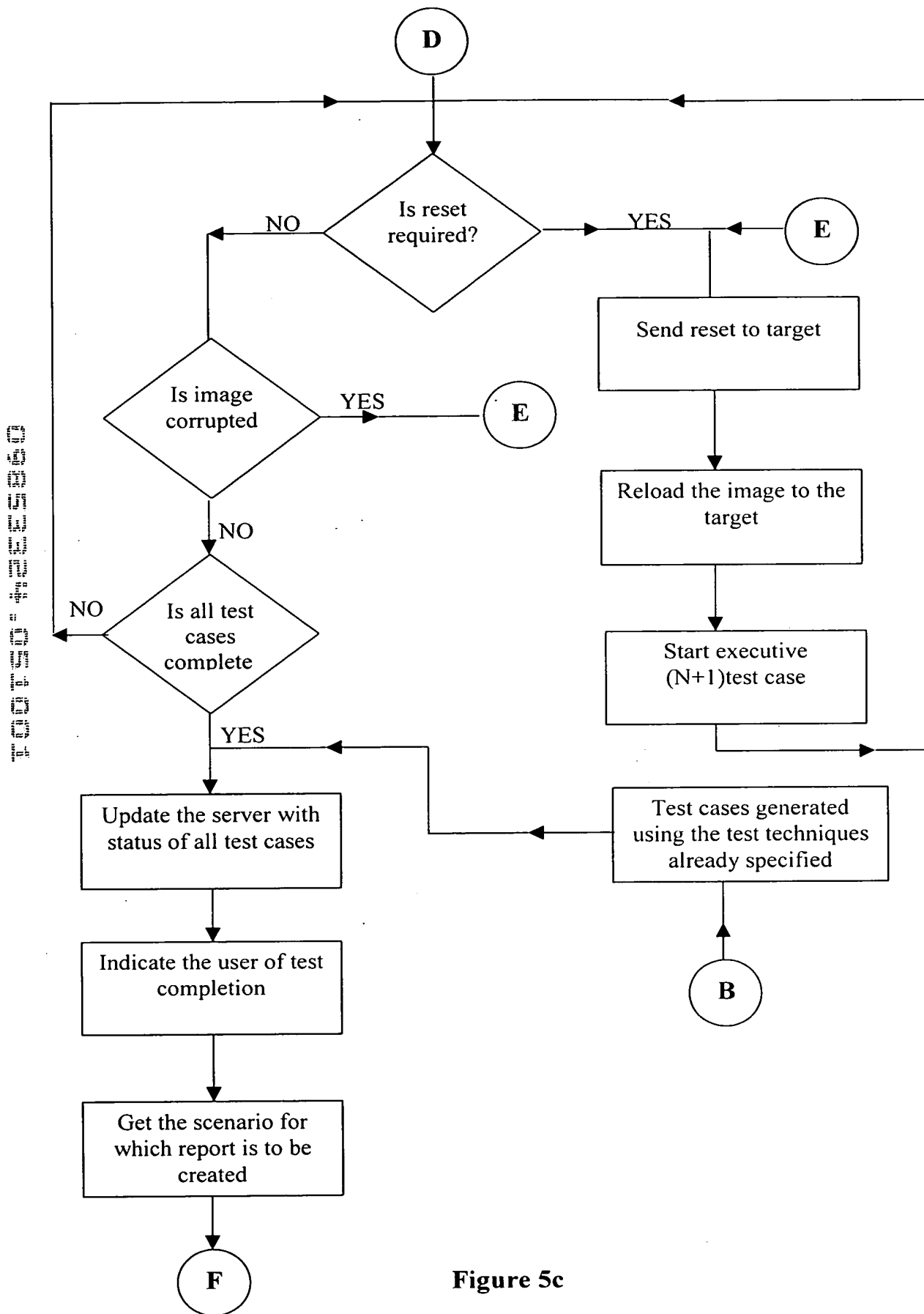


Figure 5c

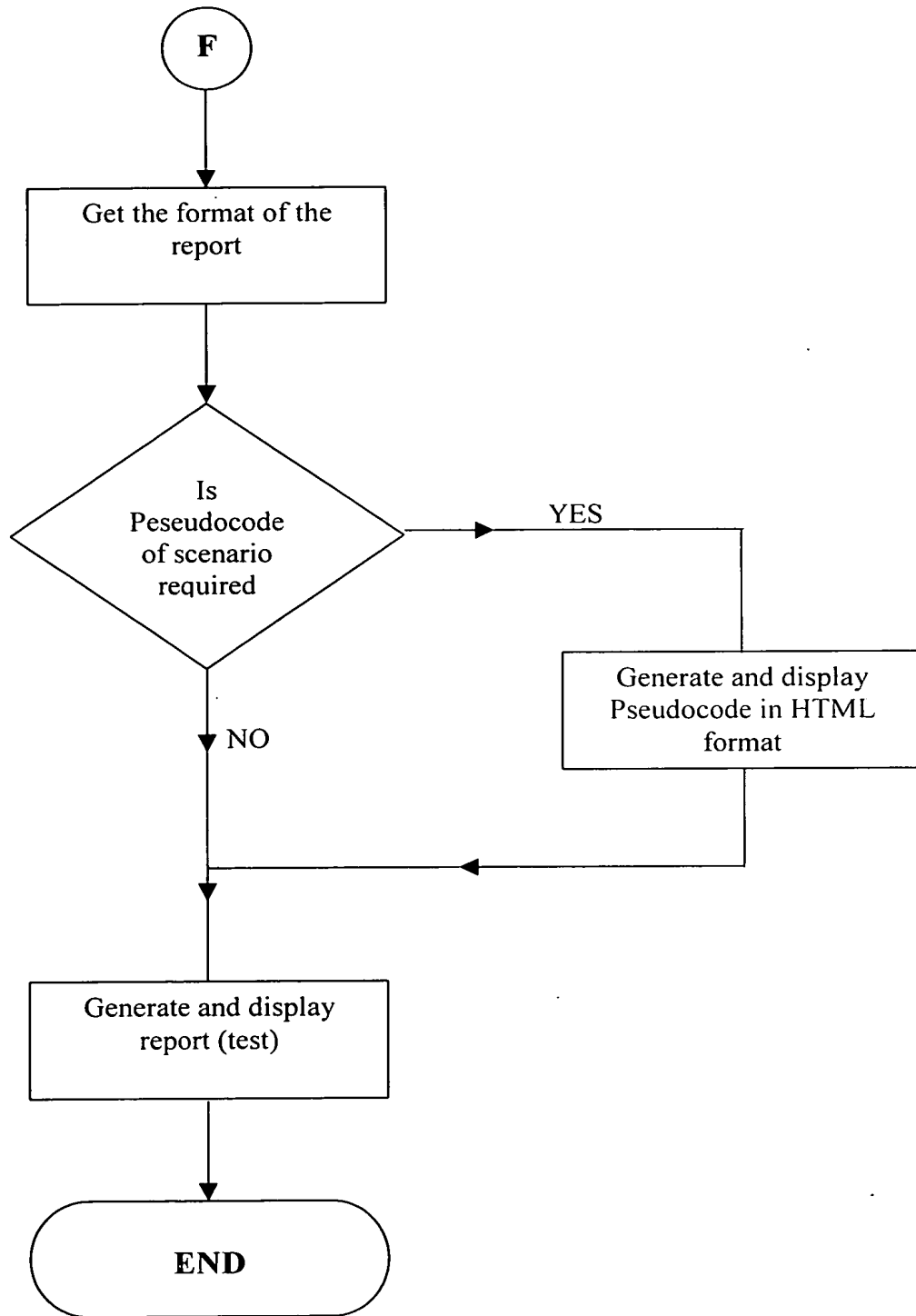


Figure 5d

TEST SCENARIO GENERATION

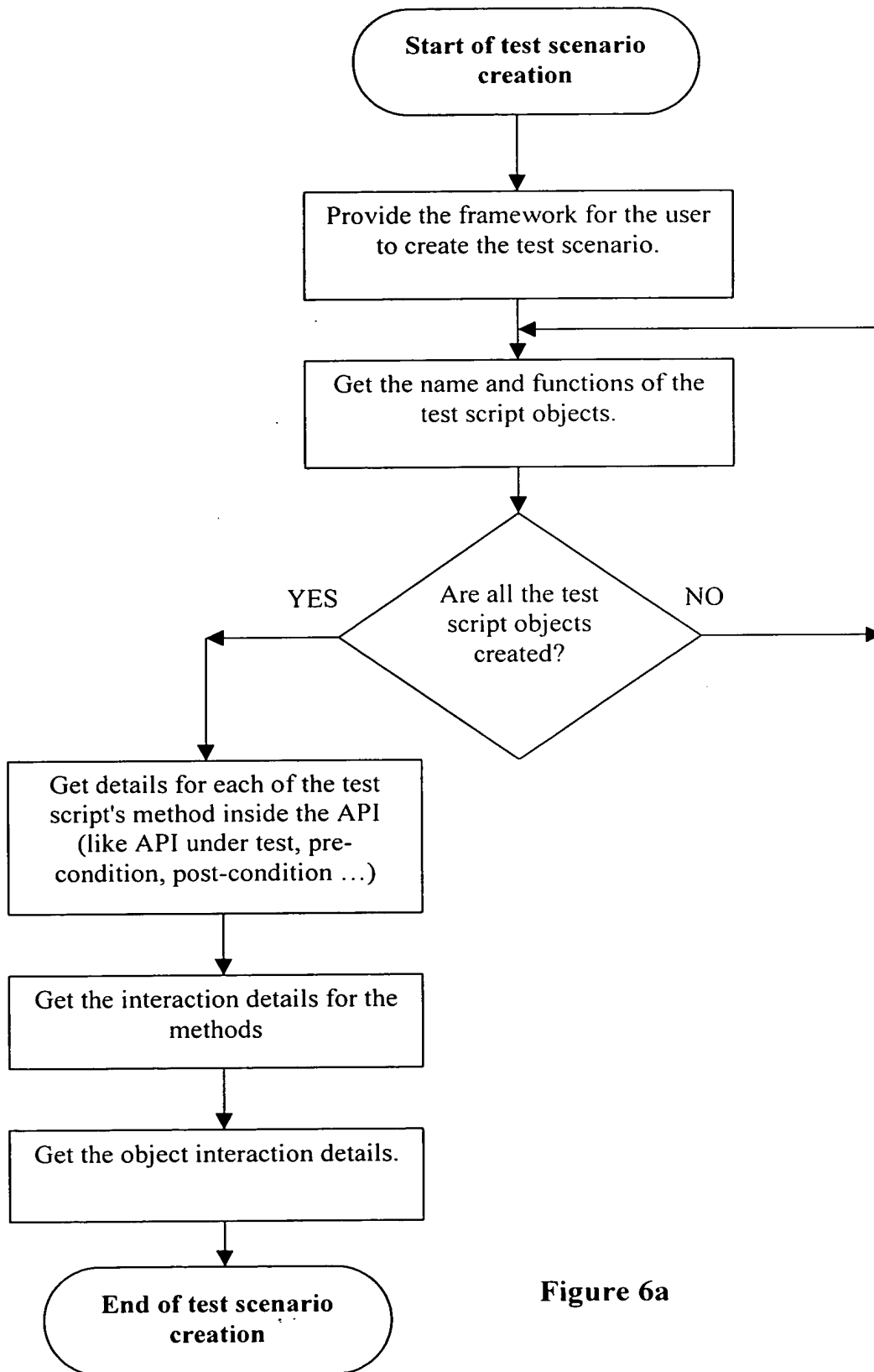


Figure 6a

TEST CASE GENERATION

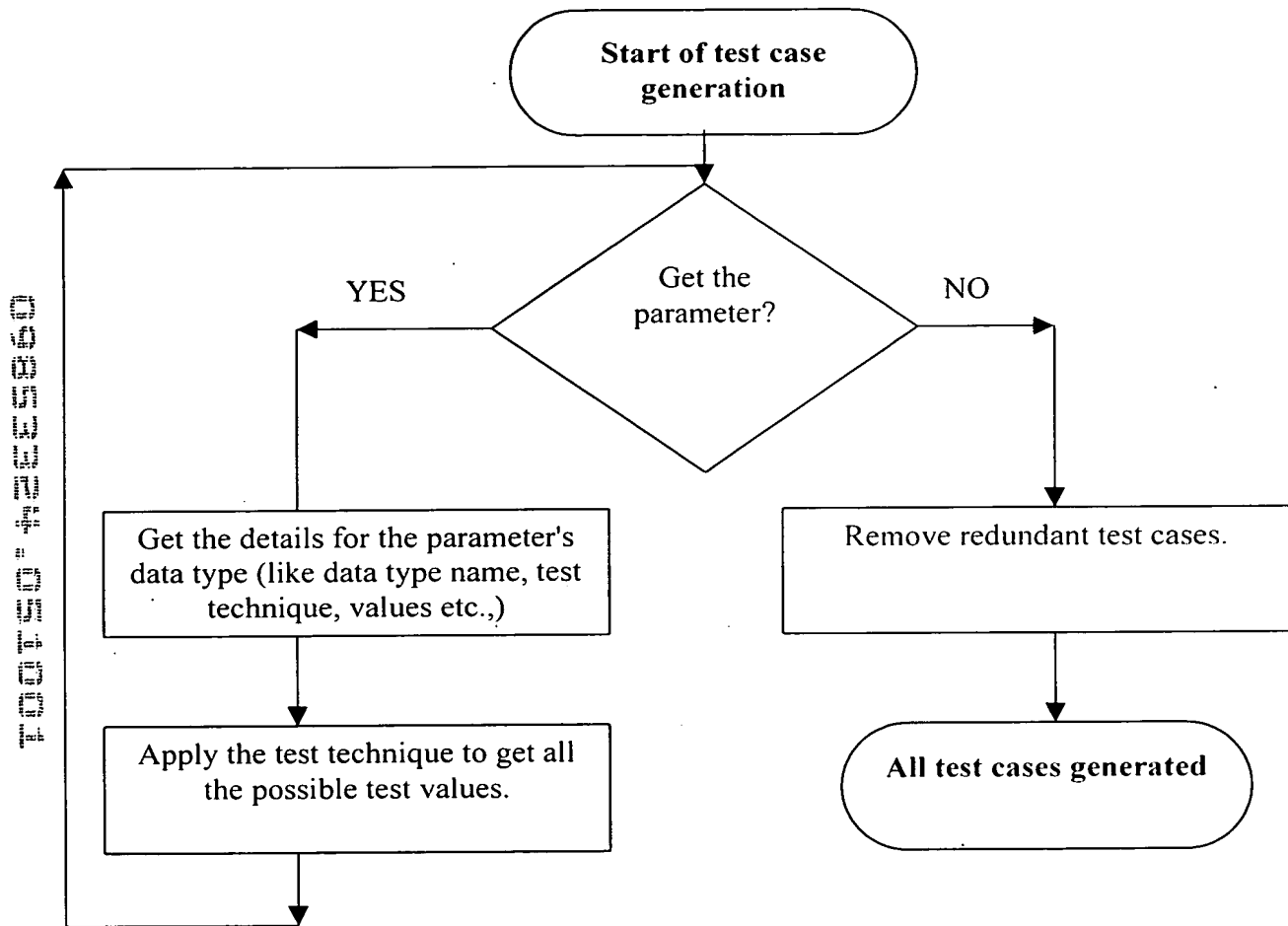


Figure 6b

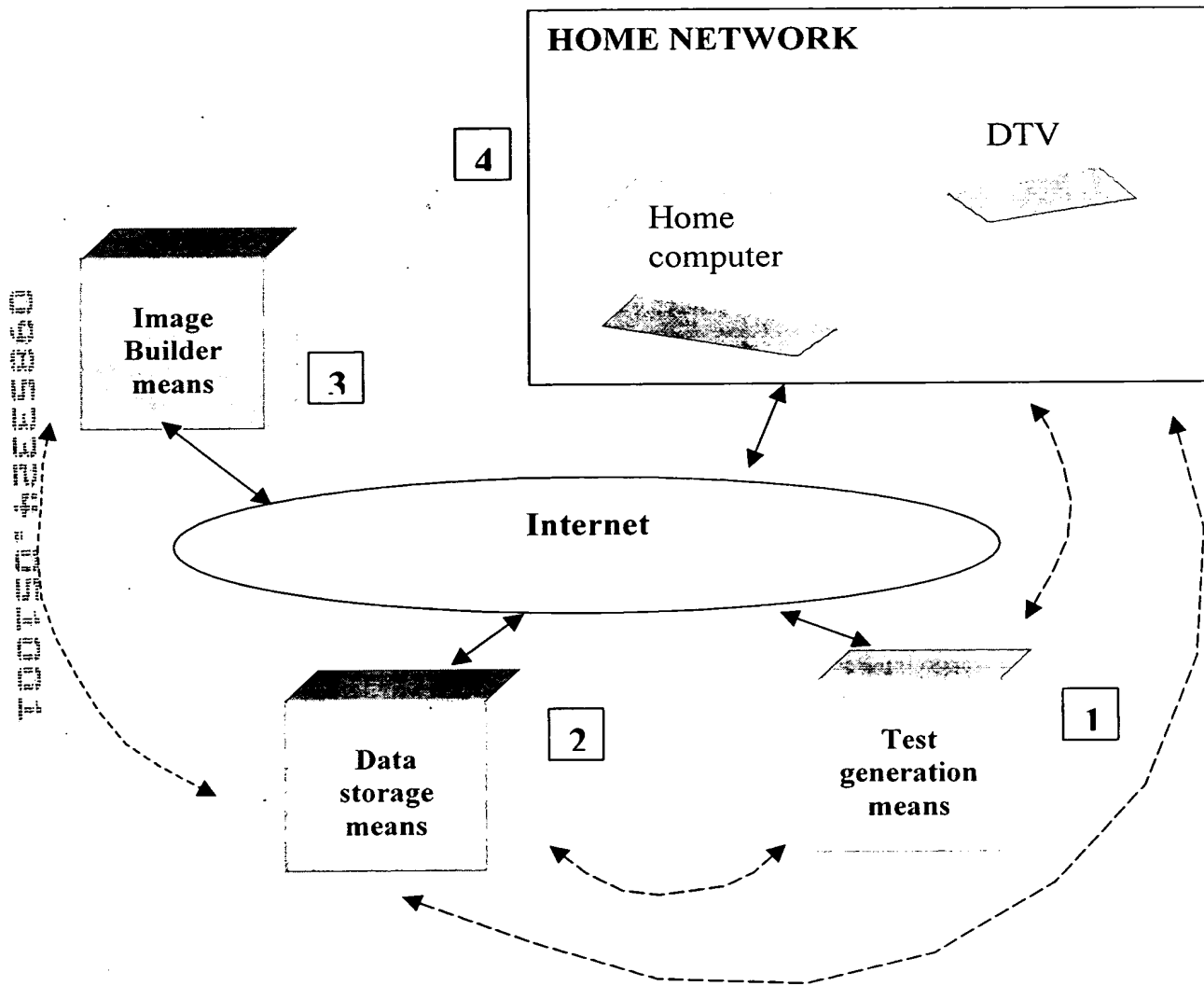


Figure 7